## **Specification**

In the Office Action, the Examiner objects to the title and suggests a new title. Applicants have amended the title in a similar manner to that suggested by the Examiner and request that this objection now be withdrawn.

## Claims Rejections - 35 USC §§102, 103

The Examiner also rejects Claims 1-4, 11-14, 21-24, 31-34, 41-44, 51-54, 61-64 and 71-74 under 35 U.S.C. §102(e), as being anticipated by Kubota et al. and Claims 5-10, 15-20, 25-30, 35-40, 45-50, 55-60, 65-70 and 75-80 under 35 U.S.C. §103(a) as being unpatentable over Kubota et al. in view of Takano et al. and Hasegawa et al. These rejections are respectfully traversed.

The claims of the present application, such as for example independent Claim 1, are directed to a display device in which gradation voltage and time gradation are both used. It is respectfully submitted that the cited references do not disclose or suggest such a display device.

In the Office Action, the Examiner alleges that <u>Kubota</u> discloses the claimed display device and in particular that the gradation voltage and time gradation requirement of the claimed invention are disclosed in "figures 1, 14, or 24, column 23, lines 15-53." Applicants respectfully disagree and submit that at the very least <u>Kubota</u> fails to disclose or suggest the claimed time gradation.

More specifically, as described in the Abstract and at col. 21, ln. 21 - col. 28, ln. 54 in Kubota,  $2^k$  decoded signals generated from one decoder 14 are inputted into selecting circuit 16 which generates a signal (period selecting signal PRD) for selecting one of the periods which were obtained by dividing one horizontal scanning period into  $2^k$  based upon k-numbered timing signals (TIM) by using the  $2^k$  decoded signals. Then, the PRD and  $2^m$  decoded signals generated from the other one decoder 14 are inputted into logic circuit 17, and logic circuit 17 generates  $2^m$  signals for the output

switch 18 so as to select one of the 2<sup>m</sup> gradation power source lines PL. As a result, a desired gradation voltage V is outputted to the source line SL during one period of  $2^k$  periods. Therefore, since Kubota selects voltage V during one period in one horizontal scanning period and adds the

voltage V to a source line, Kubota teaches only gradation voltage and not gradation time, as required

by the claims of the present application. The other references have not been cited for this feature.

Accordingly, it is respectfully submitted that the cited references fail to disclose or suggest the claimed invention. Therefore, the claims are patentable over the references and should now be allowed.

## Conclusion

It is respectfully submitted that the present application is now in a condition for allowance.

If any fee is due for this amendment, please charge our Deposit Account No. 50-1039.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,

Date: September 9, 7002

Registration No. 34,225

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Marked-up copy of the amendments herein:

## **IN THE TITLE:**

Please amend the title as follows:

Time And Voltage Gradation Driven Display Device